

Product Specification

SPECIFICATION FOR APPROVAL

() Final Specification

Title	55.0" WUXGA TFT LCD

BUYER	Syntax (Digimedia)
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC550W01
SUFFIX	A5K2

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
Please return 1 copy for you	r confirmation with
your signature and o	omments

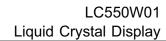
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LG. Philips LCD Co., Ltd

Ver. 0.1 Sep. 20. 2006 1 / 27







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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
0.0	Sep.20, 2006	-	Preliminary Specification(First Draft)
0.1	Sep.21, 2006	24	International Standards update
		4	
		,	





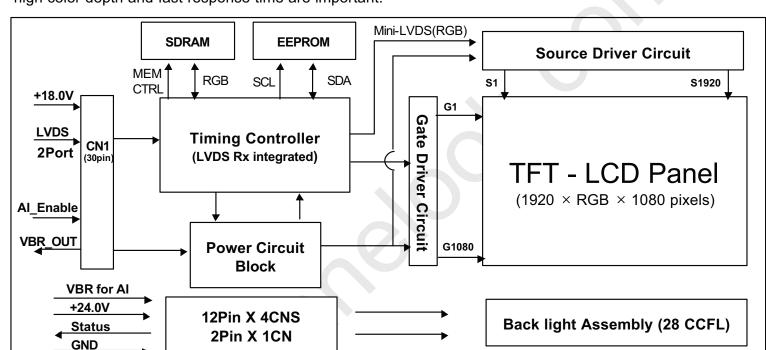
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1. General Description

The LC550W01 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs aSi Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 54.64 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

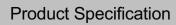
Active Corean Size	E4 C4 inches/1207 (Comm) diagonal
Active Screen Size	54.64 inches(1387.86mm) diagonal
Outline Dimension	1264.0(H) x 738.4 (V) x 49.8 mm(D) (Typ.)
Pixel Pitch	0.630 mm x 0.630 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	8-bit, 16.7 M colors
Luminance, White	500 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 176 (Typ.), U/D 176 (Typ.))
Power Consumption	Total 265.86 W (Typ.) (Logic=13.86 W, Inverter=252 W [I _{BL} =6.5mA])
Weight	30K g (Typ.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer

Liquid Crystal Display



LC550W01





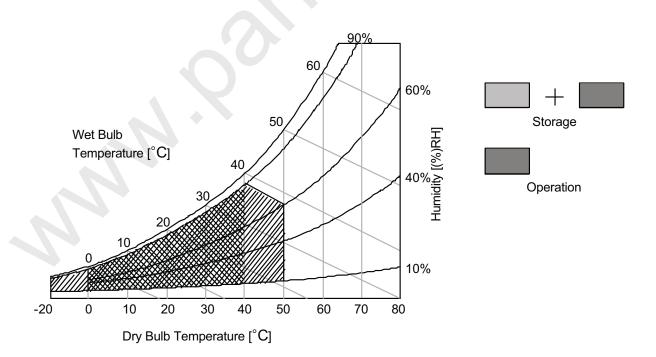
2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Cymahal	Value			Domonic
		Symbol	Min	Max	Unit	Remark
Power Input LCM		VLCD	-0.3	+19.8	VDC	at 25 ± 2 °C
Voltage Backlight inverter		VBL	21.6	+27.0	VDC	
ON/OFF Control Voltage		VON/OFF	-0.3	+5.25	VDC	
Brightness Control Voltage		VBr	0	+3.3	VDC	
Operating Temperature		Тор	0	+40	°C	
Storage Temperature		Тѕт	-20	+50	°C	Note 4
Operating Ambient Humidity		Нор	10	90	%RH	Note 1
Storage Humidity		Нѕт	10	90	%RH	

Note: 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max. and no condensation of water.





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3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the CCFL backlight and inverter circuit.

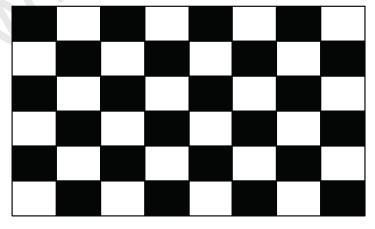
Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note	
raiametei	Symbol	Min	Тур Мах			Offic
Circuit :						
Power Input Voltage	VLCD	17.0	18.0	19.0	VDC	
Power Input Current	ILCD	539	770	1001	mA	1
Fower input Guiterit	ILCD	735	1050	1365	mA	2
Power Consumption	PLCD	9.7	13.86	18.02	Watt	1
Rush current	IRUSH	_	_	3.0	А	3

Note : 1. The specified current and power consumption are under the V_{LCD} =18.0V, 25 \pm 2°C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_{ij} is the frame frequency.

- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 1ms (min.).

White: 255Gray Black : 0Gray



Mosaic Pattern(8 x 6)





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Table 2. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		0		Value	11. %			
		Symbol	Min	Тур	Max	Unit	Note	
Inverter:	_							
Power Input Voltage			VBL	22.8	24.0	25.2	VDC	1
Power Input Currer	Power Input Current			9.0	10.5	12	Α	1
Power Consumption	Power Consumption			214	252	290	W	1
Input Voltage for Brightness Adjust		VBR	0		3.3	VDC	2	
Control System	On/Off	On	V on	3.5	5.0	5.25	VDC	
Signals		Off	V off	-0.3	0.0	0.8	VDC	
Lamp :						*		
Life Time				50,000			Hrs	3

Notes:

1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120Min at 25±2°C

The specified current and power consumption are under the typical supply Input voltage, it is total power consumption.

The ripple voltage of the power supply input voltage is under 0.5 Vp-p.

2. Brightness Control.

This VBR Voltage control brightness.

VBR Voltage Function					
3.3V Maximum Brightness (100%)					
0V Minimum Brightness.(30~40%)					

3. The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ± 2 °C. Specified value is when lamp is aligned horizontally.





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3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 30-pin connector is used for the module electronics and four 12-pin connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-XL30SSL-HF(manufactured by JAE) or equivalent
- Mating Connector: FI-30C2L(JAE) or compatible

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

ible 3.	DIE 3. MODULE CONNECTOR(CNT) PIN CONFIGURATION									
Pin	Signal assignment	Pin	Signal assignment							
1	GND	16	SECOND CHANNEL 0+							
2	VBR_OUT	17	SECOND CHANNEL 1-							
3	Al_Enable	18	SECOND CHANNEL 1+							
4	GND	19	SECOND CHANNEL 2-							
5	FIRST CHANNEL 0-	20	SECOND CHANNEL 2+							
6	FIRST CHANNEL 0+	21	SECOND CLOCK CHANNEL C-							
7	FIRST CHANNEL 1-	22	SECOND CLOCK CHANNEL C+							
8	FIRST CHANNEL 1+	23	SECOND CHANNEL 3-							
9	FIRST CHANNEL 2-	24	SECOND CHANNEL 3+							
10	FIRST CHANNEL 2+	25	GND							
11	FIRST CLOCK CHANNEL C-	26	GND							
12	FIRST CLOCK CHANNEL C+	27	GND							
13	FIRST CHANNEL 3-	28	V _{LCD} (18V)							
14	FIRST CHANNEL 3+	29	VLCD(18V)							
15	SECOND CHANNEL 0-	30	VLCD(18V)							



Rear view of LCM

Note: 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the IEA 664 Standard.





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Table 4. Required signal assignment for Flat Link (TI:SN75LVDS83) Transmitter								
Pin	Pin Name	Require Signal	Pin	Pin Name	Require Signal			
1	VCC	Power Supply for TTL Input	29	GND	Ground pin for TTL			
2	D5	TTL Input(R7)	30	D26	TTL Input(DE)			
3	D6	TTL Input(R5)	31	TxCLKIN	TTL Level clock Input			
4	D7	TTL Input(G0)	32	PWR DWN	Power Down Input			
5	GND	Ground pin for TTL	33	PLL GND	Ground pin for PLL			
6	D8	TTL Input(G1)	34	PLL VCC	Power Supply for PLL			
7	D9	TTL Input(G2)	35	PLL GND	Ground pin for PLL			
8	D10	TTL Input(G6)	36	LVDS GND	Ground pin for LVDS			
9	VCC	Power Supply for TTL Input	37	TxOUT3+	Positive LVDS differential data output3			
10	D11	TTL Input(G7)	38	TxOUT3-	Negative LVDS differential data output3			
11	D12	TTL Input(G3)	39	TxCLKOUT+	Positive LVDS differential clock output			
12	D13	TTL Input(G4)	40	TxCLKOUT-	Negative LVDS differential clock output			
13	GND	Ground pin for TTL	41	TxOUT2+	Positive LVDS differential data output2			
14	D14	TTL Input(G5)	42	TxOUT2-	Negative LVDS differential data output2			
15	D15	TTL Input(B0)	43	LVDS GND	Ground pin for LVDS			
16	D16	TTL Input(B6)	44	LVDS VCC	Power Supply for LVDS			
17	VCC	Power Supply for TTL Input	45	TxOUT1+	Positive LVDS differential data output1			
18	D17	TTL Input(B7)	46	TxOUT1-	Negative LVDS differential data output1			
19	D18	TTL Input(B1)	47	TxOUT0+	Positive LVDS differential data output0			
20	D19	TTL Input(B2)	48	TxOUT0-	Negative LVDS differential data output0			
21	GND	Ground pin for TTL Input	49	LVDS GND	Ground pin for TTL			
22	D20	TTL Input(B3)	50	D27	TTL Input(R6)			
23	D21	TTL Input(B4)	51	D0	TTL Input(R0)			
24	D22	TTL Input(B5)	52	D1	TTL Input(R1)			
25	D23	TTL Input(RSVD)	53	GND	Ground pin for TTL			
26	VCC	Power Supply for TTL Input	54	D2	TTL Input(R2)			
27	D24	TTL Input(HSYNC)	55	D3	TTL Input(R3)			
28	D25	TTL Input(VSYNC)	56	D4	TTL Input(R4)			

Notes: 1. Refer to LVDS Transmitter Data Sheet for detail descriptions.

2. 7 means MSB and 0 means LSB at R,G,B pixel data



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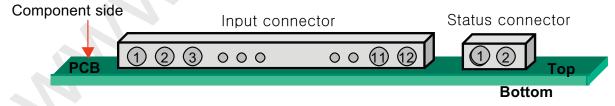
3-2-2. Backlight Inverter

- Inverter Connector : S12B-PH-SM3(manufactured by JST) or Equivalent

- Mating Connector : PHR-12 or Equivalent

Table 5. INVERTER CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	Master	Slave	Note		
1	VBL	Power Supply +24.0V	VBL	VBL			
2	VBL	Power Supply +24.0V	VBL	VBL			
3	VBL	Power Supply +24.0V	VBL	VBL			
4	VBL	Power Supply +24.0V	VBL	VBL			
5	VBL	Power Supply +24.0V	VBL	VBL			
6	GND	POWER GND	GND	GND			
7	GND	POWER GND	GND	GND			
8	GND	POWER GND	GND	GND	1		
9	GND	POWER GND	GND	GND			
10	GND	POWER GND	GND	GND			
11	VBR	0V ~ 3.3V	VBR	Don't care	2		
12 On/Off		0V ~ 5.0V	On/Off	Don't care	3		
Option Pin(Lamp Open Status Detection)							
1	GND	POWER GND	GND				
2 Status Upper 3.0V(Normal), Under 0.7V(Abnormal)			Status				



Note: 1. GND should be connected to the LCD module's metal frame.

2. Minimum Brightness: VBR = 0.0V Maximum Brightness: VBR = 3.3V

3. Von : $3.5 \sim 5.0 \text{V}$ VOFF: 0.0 ~ 0.5V





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3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

Table 6. TIMMING TABLE

Table 0.	TIMMING TABLE						
	ITEM	SYMBOL	Min.	Тур.	Max.	Unit	Notes
	Period	t _{CLK}	12.98	13.47	14.29	ns	
Clock	Frequency	f _{CLK}	70	74.25	77	MHz	=148.5 / 2
	Horizontal total	t _{HT}	1060	1100	1280	Pixel	(Dual)
	Horizontal period	t _{HP}	-	14.81	-	usec	
Hsync	Hsync frequency	f _H	-	67.5	-	KHz	
	Hsync width	t _{wH}	8	30	-	Pixel	
	Vertical total	t _{vT}	1091	1125	1149	Line	
Vsync	Vertical period	t _{vP}	-	16.67	-	msec	
	Vsync frequency	f _V	57	60	63	Hz	
	Vsync width	t _{wv}	2	5	-	Line	
	Horizontal valid	t _{HV}	960	960	960		
	Horizontal back porch	t _{HBP}	8	96	-	5	
	Horizontal front porch	t _{HFP}	8	14	-	Pixel	
DE	Horizontal blank	-	100	140	-		
DE	Vertical valid	t _{vv}	1080	1080	1080		
	Vertical back porch	t _{VBP}	4	36	-	1 (
	Vertical front porch	t _{VFP}	2	4	-	Line	
	Vertical blank	-	11	45	-		

Both Hsync Period and Hsync Width should be even number times of tCLK. Otherwise, display control Note: signal can be asynchronous. Besides theses, Hsync, Vsync and DE(Data Enable) signals should be used.

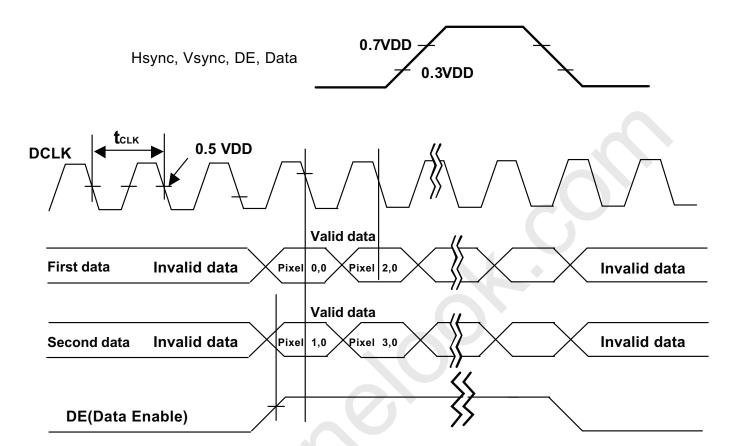
- 1. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.
- 2. Not only Vsync but Hsync have to keep the above mentioned timing specification.

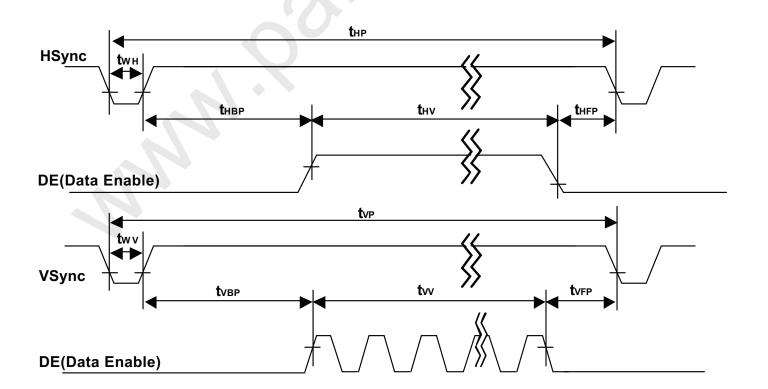




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3-4. Signal Timing Waveforms







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3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table7.	COLOR DATA I	REF	ER	ENC	CE																				
												Inpu	ut Co	olor	Data	a									
	Color				RE	D			_				GRE	EEN			_				BL	UE			
		M								MS								MS							SB
	I	+	' R6	R5	R4	R3		R1 F			G6	G5	G4	G3	G2	G1 (B6	B5	B4	В3	B2	B1 I	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																									
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																									
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1





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3-6. Power Sequence

3-6-1. LCD Driving circuit

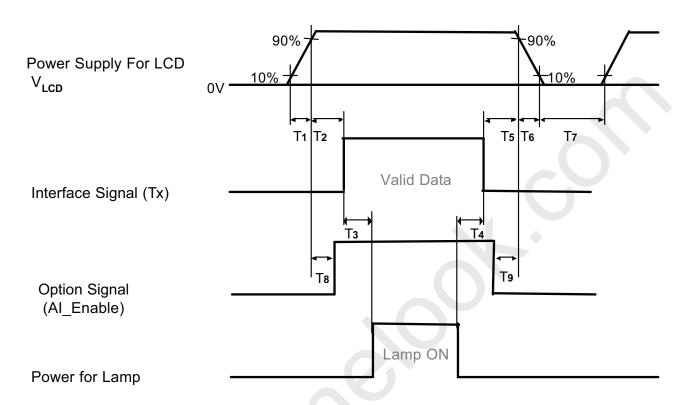


Table 8. POWER SEQUENCE

Donomotor		Unit				
Parameter	Min	Тур	Max	Unit		
T1	0.5	-	20	ms		
T2	0.5	-	50	ms		
T3	200	-	-	ms		
T4	200	-	-	ms		
T5	0.5	-	50	ms		
T6	-	-	300	ms		
T7	2.0	-	-	s		
T8		ms				
Т9		0 < T9 < T5				

Note:

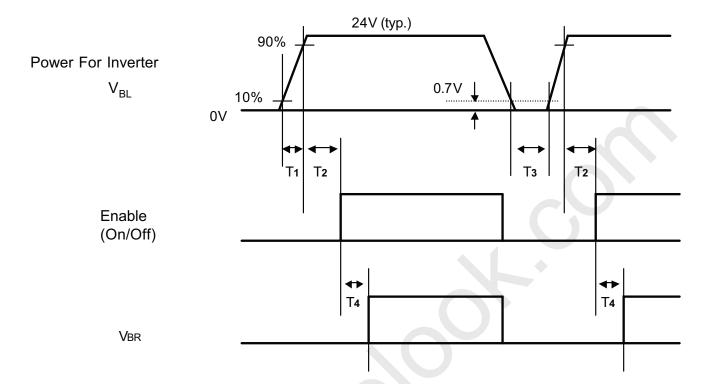
- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
- 3. The case when the T2/T5 exceed maximum specification, it operates protection pattern(Black pattern) till valid signal inputted. There is no reliability problem.
- 4. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.



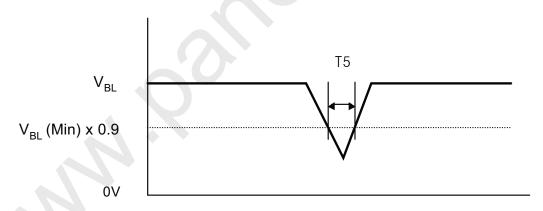


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3-6-2. On/Off for Inverter



3-6-3. Deep condition for Inverter



DIE 9 POWER SEQUENCE FOR INVERTER

Table 9. POWER SEQUI	able 9. FOWER SEQUENCE FOR INVERTER									
		Value		11.76	Damania					
Parameter	Min Typ Max		Max	Unit	Remark					
T1	20	-	-	ms	After Inverter's connected					
T2	500	-	-	ms						
Т3	200	-	-	ms						
T4	0	-	-	ms						
T5	-	-	10	ms	V_{BI} (Min) x 0.9					





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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 2 Hrs in a dark environment at 25 ±2 °C. The specified optical values are measured at an approximate 50cm distance from the LCD surface on condition that viewing angle of Φ and θ equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.

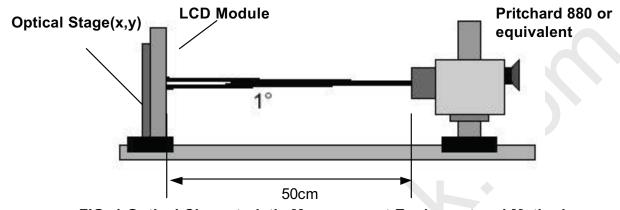


FIG. 1 Optical Characteristic Measurement Equipment and Method

		L CHARACTE	_		Value	,	Dclk=148.5MH		
Pa	ramet	er	Symbol	Min	Тур	Max	Unit	Note	
			CR	400	550	-			
Contrast Ratio)		CR with AI	800	1200			1	
Surface Lumir	nance,	white	L _{WH}	400	500		cd/m ²	2	
Luminance Va	ariation	1	δ _{WHITE} 5P			1.3		3	
Doonanaa Tim		Rise Time	Tr_R	-	7	12			
Response Tim	ie	Decay Time	Tr_{D}	-	9	13	ms	4	
		G-	to-G	-	10	16			
		RED	Rx		0.640				
		Ry		0.342					
		GREEN	Gx	Тур	0.285	Typ +0.03			
Color Coordina	ates		Gy		0.607				
[CIE1931]		BLUE	Bx	-0.03	0.147				
			Ву		0.065				
		WHITE	Wx		0.284				
			Wy		0.295				
Viewing Angle	(CR>	10)							
×	axis, ı	right(φ=0°)	θr	85	88	-		İ	
×	axis, I	eft (φ=180°)	θl	85	88	-		_	
y axis, up (∳=90°)		θи	85	88	-	degree	5		
у	axis,	down (φ=270°)	θd	85	85 88 -				
Crov Sools			Without Al						
Gray Scale			With AI					6	

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Note:

Contrast Ratio(CR) is defined mathematically as :

Surface Luminance with all white pixels

Surface Luminance with all black pixels

Measure Position: Center 1-point.

- 2. Surface Luminance(L_{WH}) is the luminance value measured at an approximate 50cm distance from the center 1-point of LCD surface as all pixels displaying white. See FIG. 2 for more information.
- 3. The variation of surface luminance, δ WHITE is defined as:

$$\delta$$
 WHITE(5P) = Maximum($L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5}$) / Minimum($L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5}$)

Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- 4. Response time is defined as the required time for the transition from G(N) to G(M) (Rise Time, Tr_P) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 6. See Table 11 for gray scale specification

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ.)	Luminance [%] (Typ.) with Al
LO	0.17	0.08
L15	0.55	0.23
L31	0.94	0.68
L47	2.20	1.55
L63	4.21	3.16
L79	7.39	5.78
L95	11.58	9.32
L111	16.57	13.62
L127	22.36	19.32
L143	30.14	27.27
L159	39.72	38.45
L175	50.50	51.33
L191	61.48	62.50
L207	73.25	73.48
L223	84.63	84.66
L239	94.01	93.56
L255	100	100

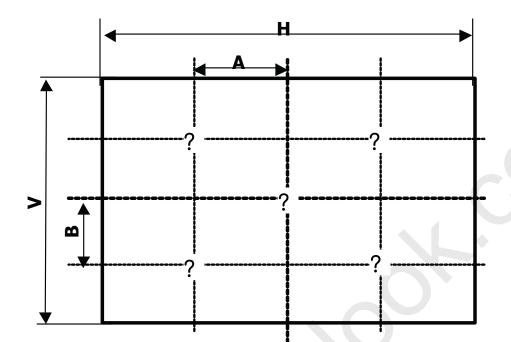


Global LCD Panel Exchange Center

LC550W01 Liquid Crystal Display

Product Specification

Measuring point for surface luminance & measuring point for luminance variation.



A:H/4mm B: V/4 mm H: 1209.6 mm V: 680.4 mm @ H,V : Active Area

FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

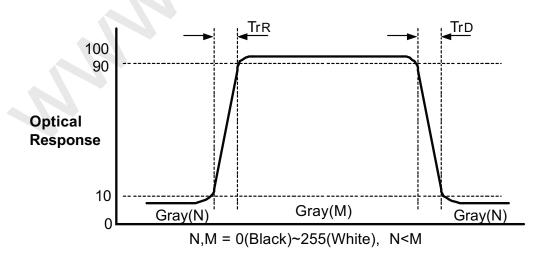


FIG. 3 Response Time

Product Specification

Dimension of viewing angle range

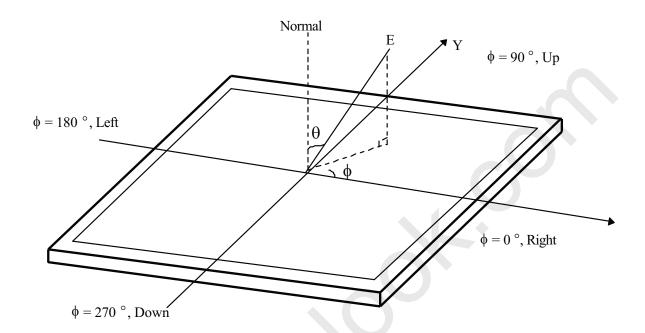


FIG. 4 Viewing Angle





Product Specification

5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

Table 12. MECHANICAL CHARACTERISTICS

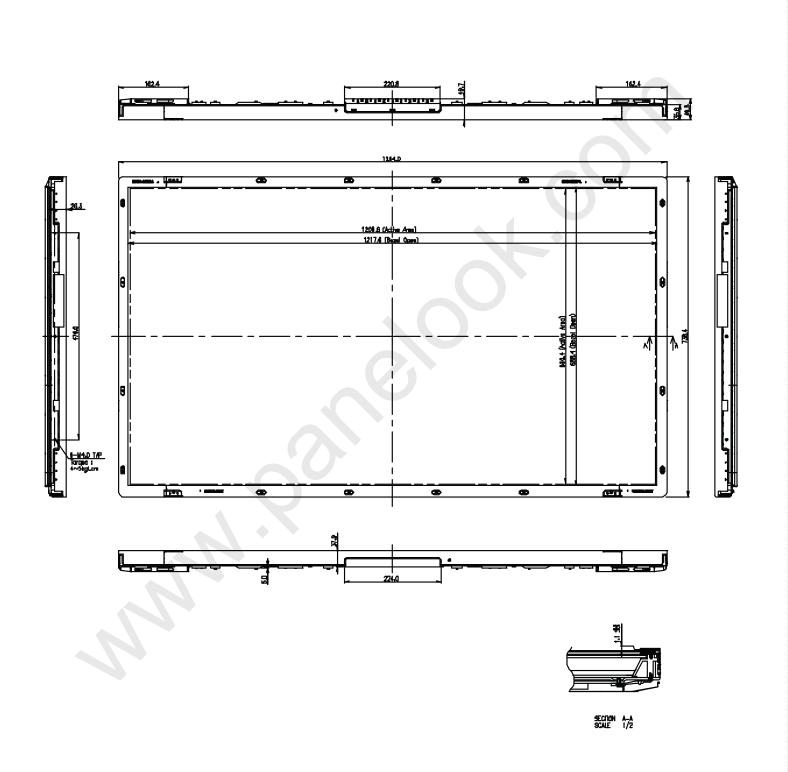
Item	Va	Value					
	Horizontal	1264.0 mm					
Outline Dimension	Vertical	738.4 mm					
	Depth	49.8 mm					
Danal Avan	Horizontal	1217.6 mm					
Bezel Area	Vertical	688.4 mm					
Astina Disulan Anas	Horizontal	1209.6 mm					
Active Display Area	Vertical	680.4 mm					
Weight	30Kg (Typ.), 31.5Kg (Max.)						
Surface Treatment Anti-glare treatment of the front polarizer							

Note: Please refer to page21 and 22 for mechanic drawings in terms of tolerance.



Product Specification

<FRONT VIEW>

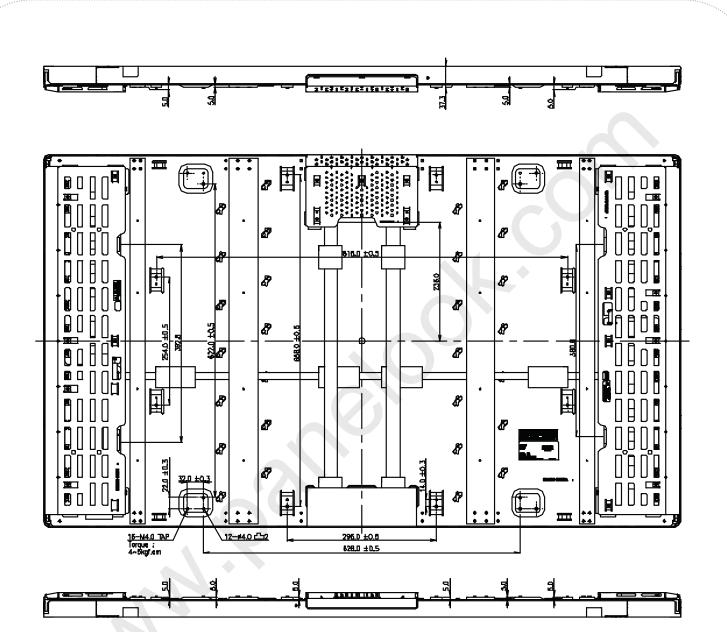






Product Specification

<REAR VIEW>





- NOTES

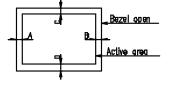
 1. Unspecified tolerances are to be ±1.0mm.

 2. The laright of mounting screw is MAX 5.5mm.

 3. Tilt and partial disposition tolerance of display area group as tolerance.

 (1) X-Direction: IA-BI ≤ 1.5mm.

 (2) Y-Direction: IC-DI ≤ 1.5mm.







Product Specification

6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition					
1	High temperature storage test	Ta= 50°C 240h					
2	Low temperature storage test	Ta= -20°C 240h					
3	High temperature operation test	Ta= 40°C 50%RH 240h					
4	Low temperature operation test	Ta= 0°C 240h					
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-500Hz Duration : X,Y,Z, 10 min One time each direction					
6	Shock test (non-operating)	Shock level : 40G Waveform : half sine wave, 2ms Direction : ±X, ±Y, ±Z One time each direction					
7	Humidity condition Operation	Ta= 40 °C, 90%RH					
8	Altitude operating storage / shipment	0 - 14,000 feet(4267.2m) 0 - 40,000 feet(12192m)					





Product Specification

7. International Standards

7-1. Safety

a) UL 60065, 7th Edition, dated June 30, 2003, Underwriters Laboratories, Inc.,

Standard for Audio, Video and Similar Electronic Apparatus.

b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association,

Standard for Audio, Video and Similar Electronic Apparatus.

c) IEC60065:2001, 7th Edition CB-scheme and EN 60065:2002,

Safety requirements for Audio, Video and Similar Electronic Apparatus...

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) CISPR22 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment. European Committee for Electrotechnical Standardization. (CENELEC), 1998 (Including A1: 2000)





Product Specification

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

Α	ВС	D	Е	F	G	Н	I	J	K	L	М	
---	----	---	---	---	---	---	---	---	---	---	---	--

A,B,C: SIZE(INCH)

D : YEAR E : MONTH

F : PANEL CODE G : FACTORY CODE H : ASSEMBLY CODE I,J,K,L,M : SERIAL NO.

Note

1. YEAR

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	Α	В	С

3. PANEL CODE

Panel Code	P1 Factory	P2 Factory	P3 Factory	P4 Factory	P5 Factory	Hydis Panel
Mark	1	2	3	4	5	Н

4. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	Hee Sung
Mark	K	O	D

5. SERIAL NO.

Year	1 ~ 99999	100000 ~
Mark	00001 ~ 99999	A0001 ~ A9999,, Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 4 pcs

b) Box Size: 1455mm X 953mm X 1015mm.





Product Specification

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental

to the polarizer.)

- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.





Product Specification

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.